N THE UNITED STATES PATENT AND TRADEMARK OFFICE

pplication of:

Daniel J. Bolda et al.

Serial No.:

09/675,858

Filed:

September 29, 2000

For:

ADJUSTABLE GAIN PRECISION

FULL WAVE RECTIFIER WITH

REDUCED ERROR

Group Art Unit:

2819

Examiner:

Jeanglaude, Jean B.

Atty. Docket: 99AB139-A/YOD

ALBR:0067--1

Assistant Commissioner for Patents Washington, D.C. 20231 CERTIFICATE OF MAILING 37 C.F.R. 1.8

I hereby certify that this correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231, on the date below:

June 12, 2002

Date

AMENDMENT AND RESPONSE TO **OFFICE ACTION MAILED MARCH 19, 2002**

Dear Sir:

In response to the Office Action mailed on March 19, 2002, please amend the referenced patent application as follows:

IN THE CLAIMS

Please amend the claim as follows:

(Amended)

The circuit of claim \$5, wherein a control circuit monitors

the digital signal and applies the control signals to the switching devices to maintain the

digital signal within a desired range.

